Docket No.: 03-0177 **PATENT** 

#### UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

By the Examiner:

Rafael Kedem

Serial No.:

Filed: June 25, 2003

Group Art Unit:

Title: METHOD AND APPARATUS FOR EXPOSING PRE-DIFFUSED IP BLOCKS IN A

SEMICONDUCTOR DEVICE FOR PROTOTYPING BASED ON HARDWARE

**EMULATION** 

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

#### **CERTIFICATION UNDER 37 C.F.R. §1.10**

I hereby certify that this is an Original Application Transmittal and the documents referred to as enclosed therein are being deposited with the United States Postal Service on this date, June 25, 2003 in an envelope as "Express Mail Post Office to Addressee", Mailing Label Nº EV 338 284 375 US, with sufficient postage, addressed to: MS Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

DATED: June 25, 2003

Dear Sir:

## INFORMATION DISCLOSURE STATEMENT

Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98(a)(1).

A copy of each of these items on PTO-1449 is supplied herewith.

Please direct all correspondence and telephone calls to:

24210

24319
PATENT TRADEMARK OFFICE

LEO PETERS

INTELLECTUAL PROPERTY LAW DEPARTMENT

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DATED: June 25, 2003.

Respectfully submitted, Rafael Kedem,

LSI Logic Corporation,

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Æeg. № 48,063

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Serial No.:

Applicant: Filing Date: Rafael Kedem June 25, 2003

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE

Group:

Atty. Docket No.: 03-0177

Reference Designation

**STATEMENT** 

# **U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA	6,347,395	02/12/02	Payne et al.	716	18	12/18/98
ABA	6,263,484	07/17/01	Yang	716	18	10/01/98
ACA						
ADA		-				
AEA						
AFA						
AGA						
AHA						
AIA						
AJA						

## **FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation <u>Yes No</u>
AKA						
ALA						
AMA						
ANA		·				
AOA						

OTHER A	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
Examiner							
Initial							
APA							
AQA							
ARA							
Examiner:	Date Considered:	_					
	ed, whether nor not citation is in conformance with MPEP 609; Draw line through isidered. Include copy of this form with next communication to applicant.						